

009001" 28248960

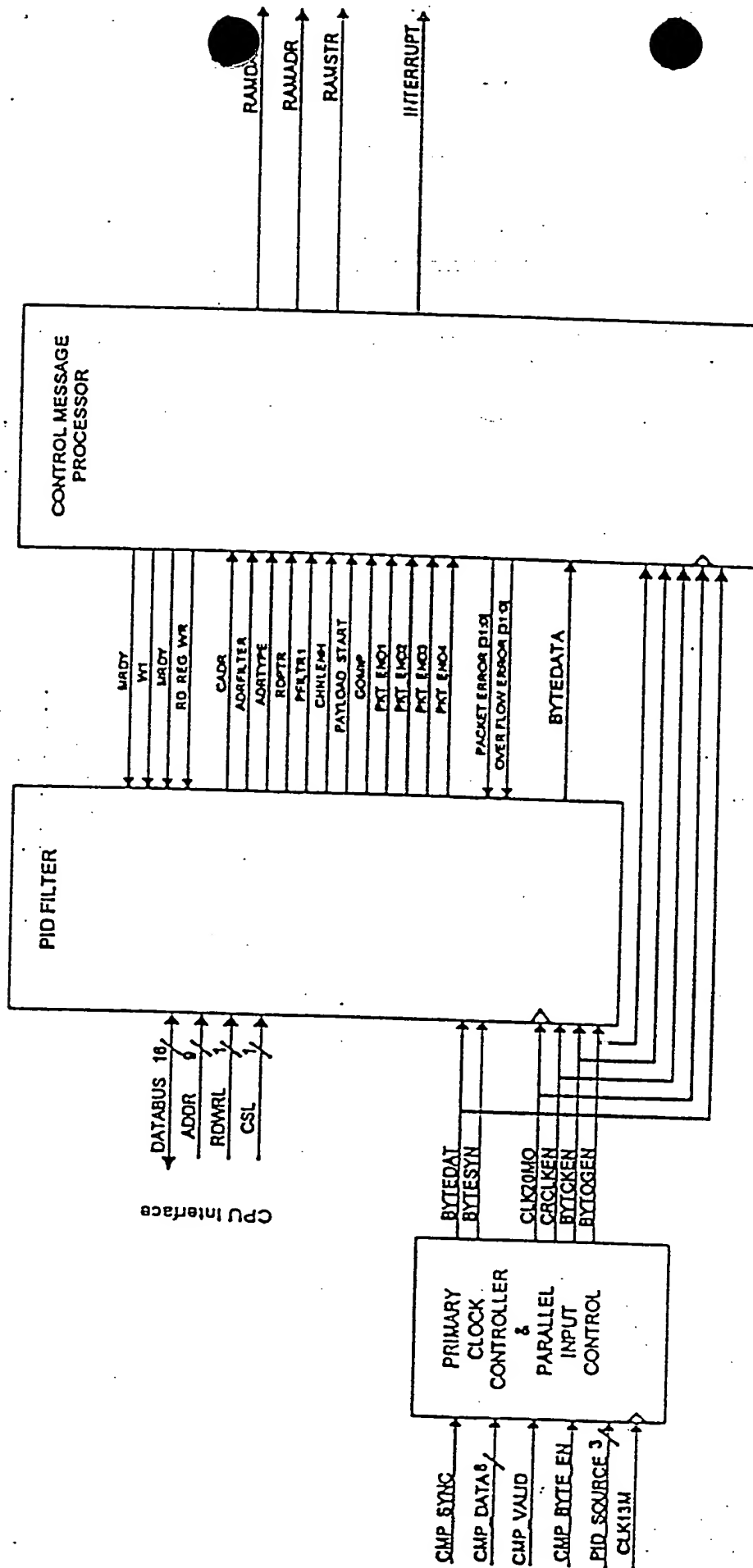
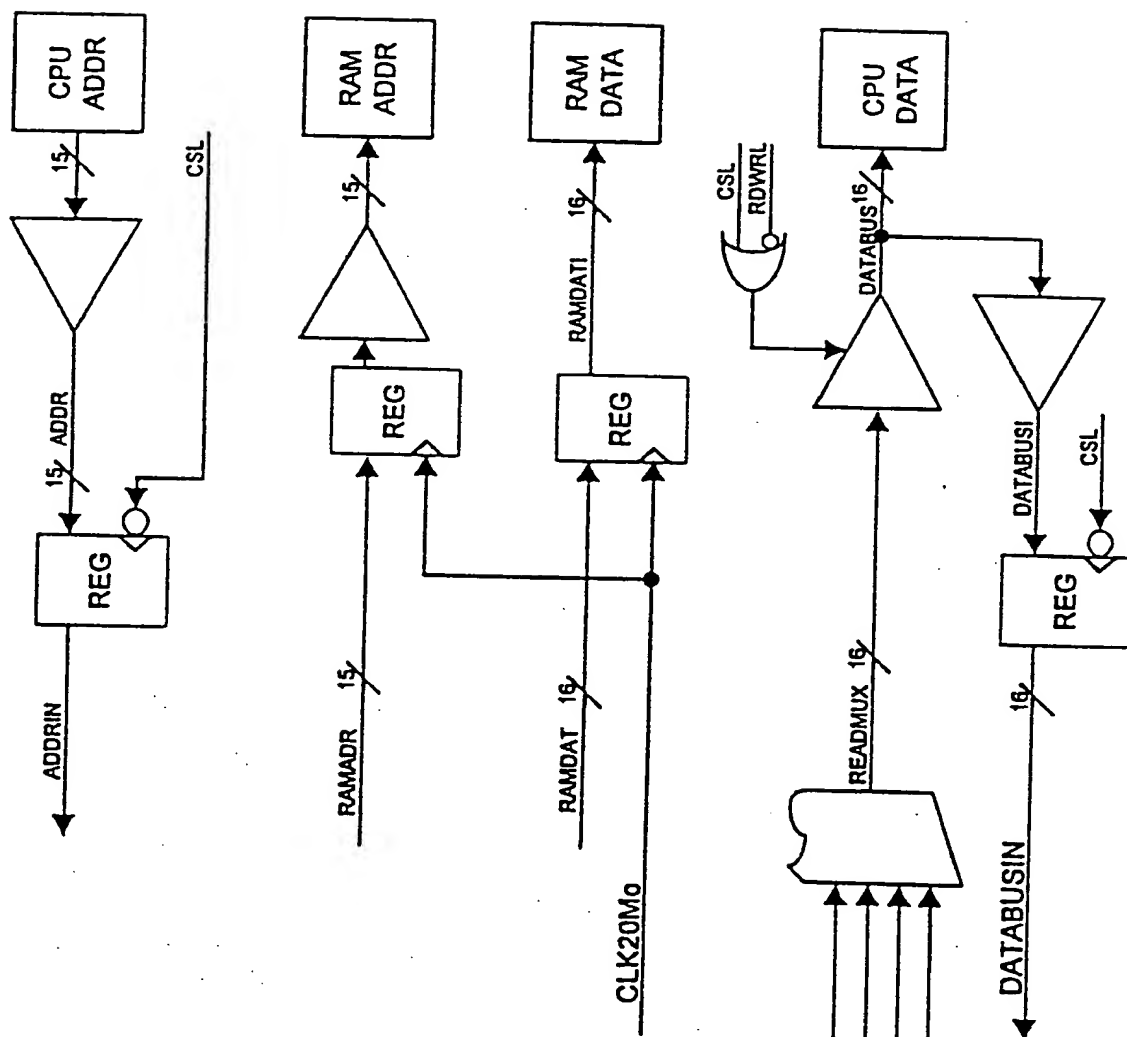


Figure 1A

Figure 13



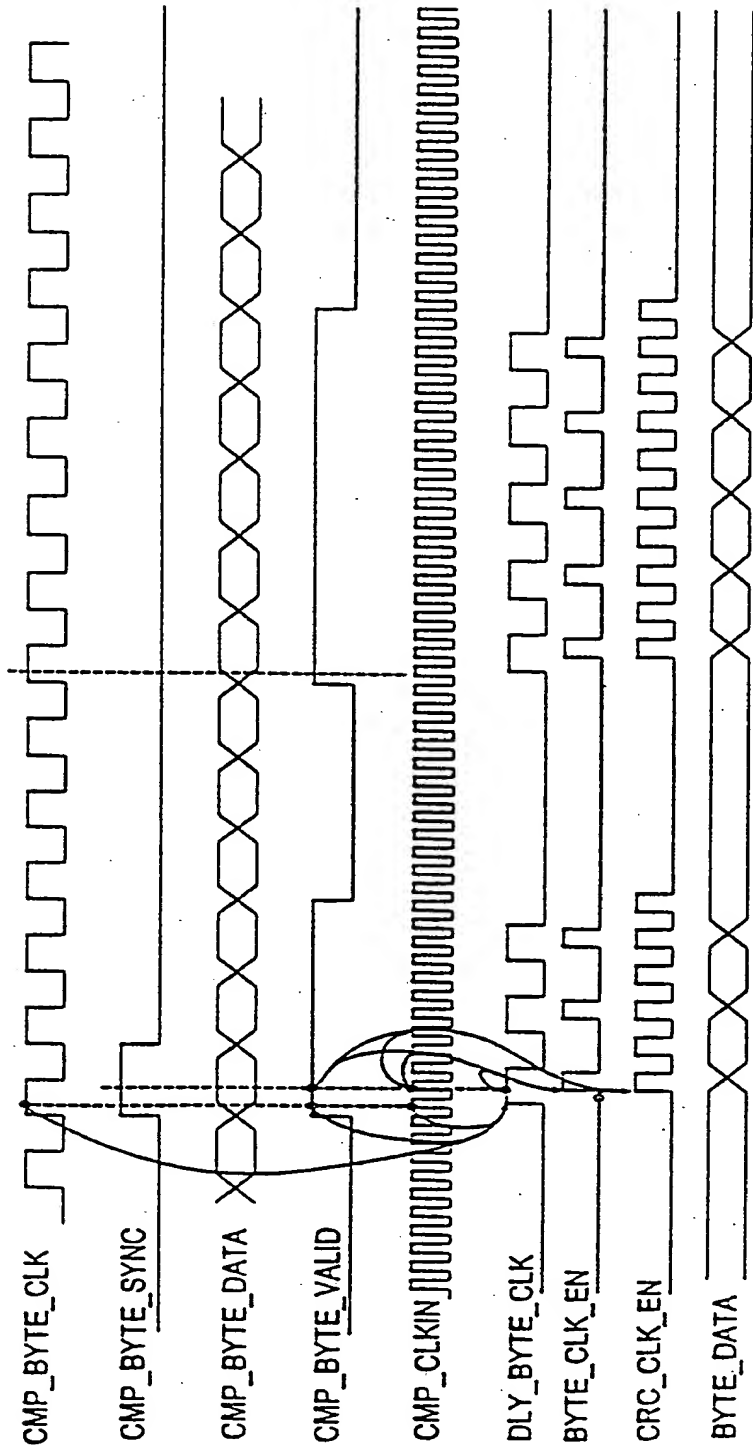


Figure 2

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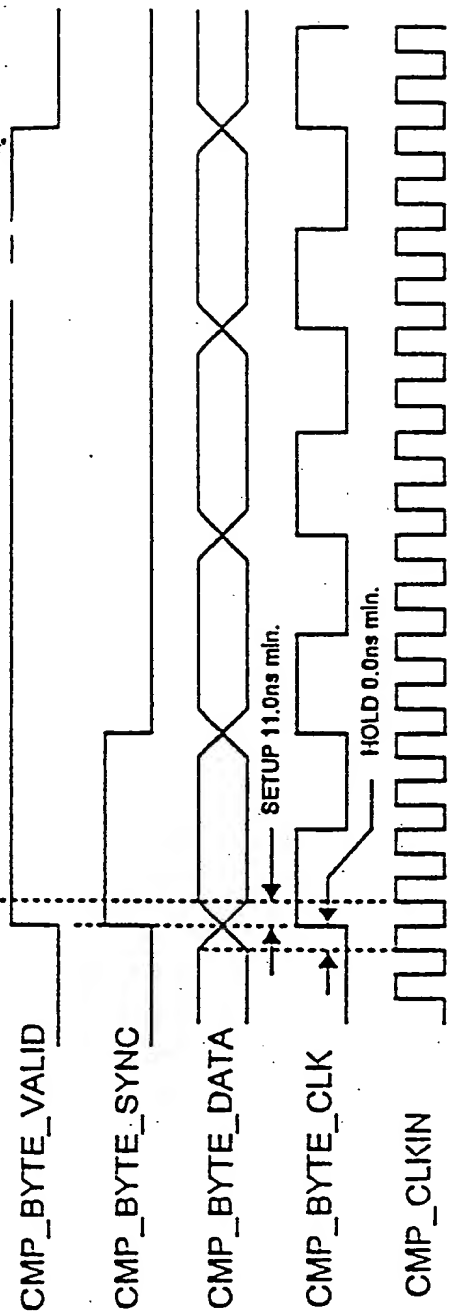


Figure 3

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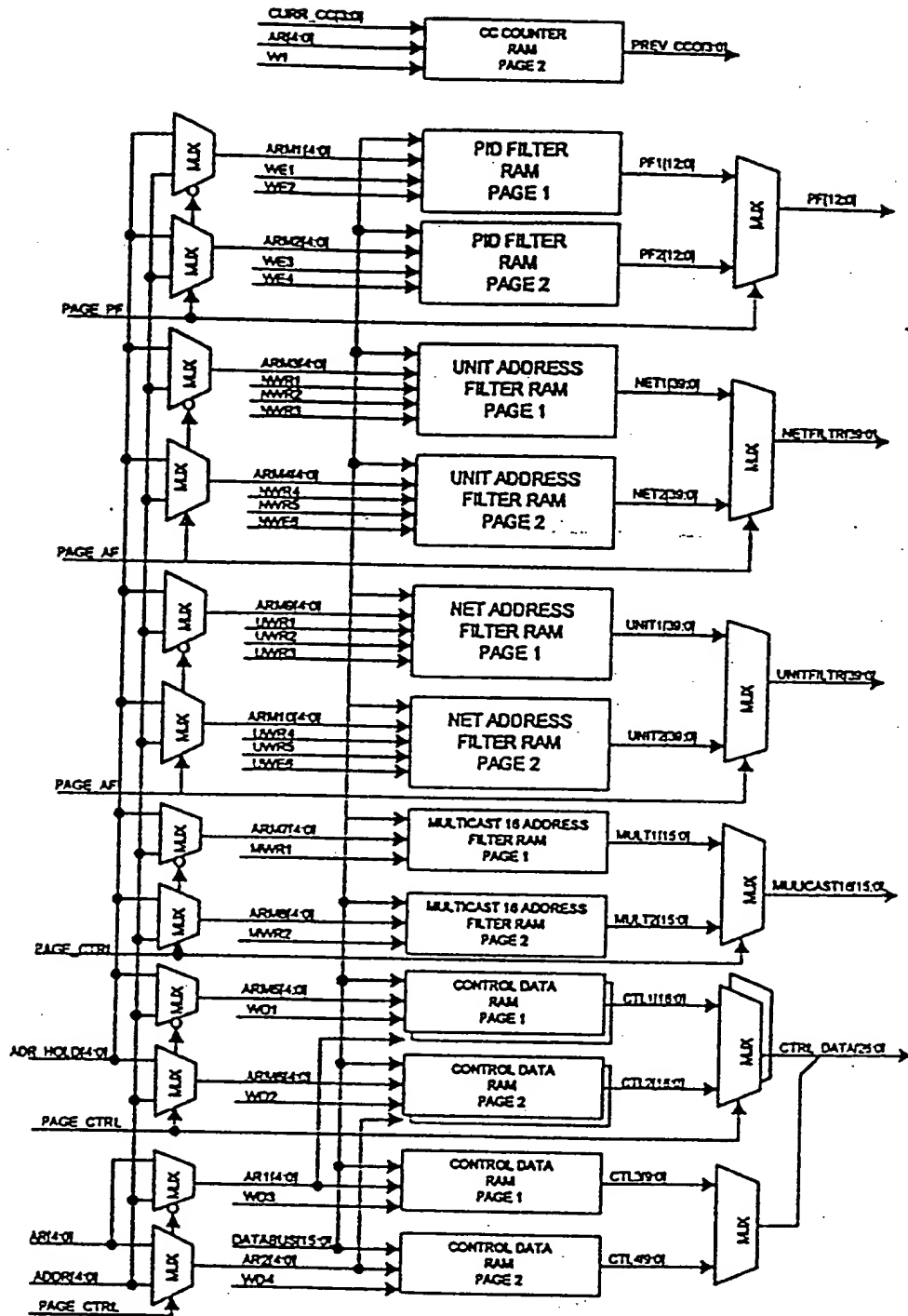


Figure 4A

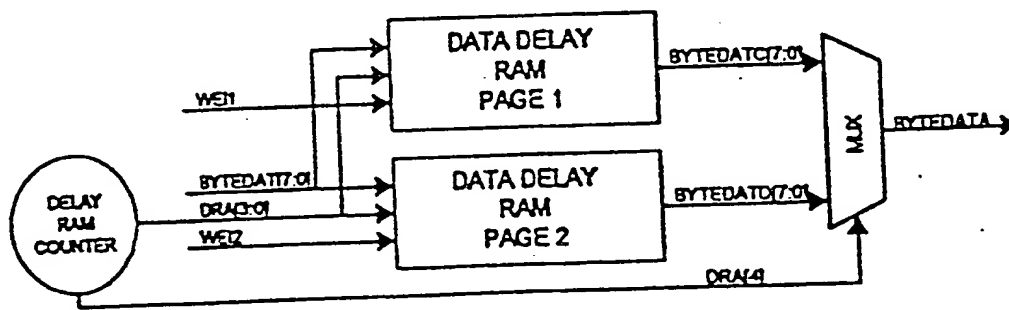
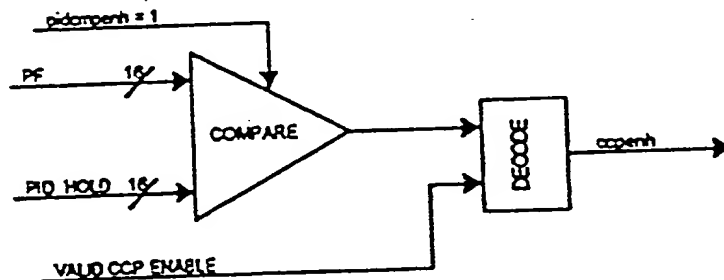
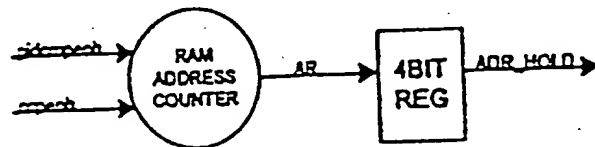
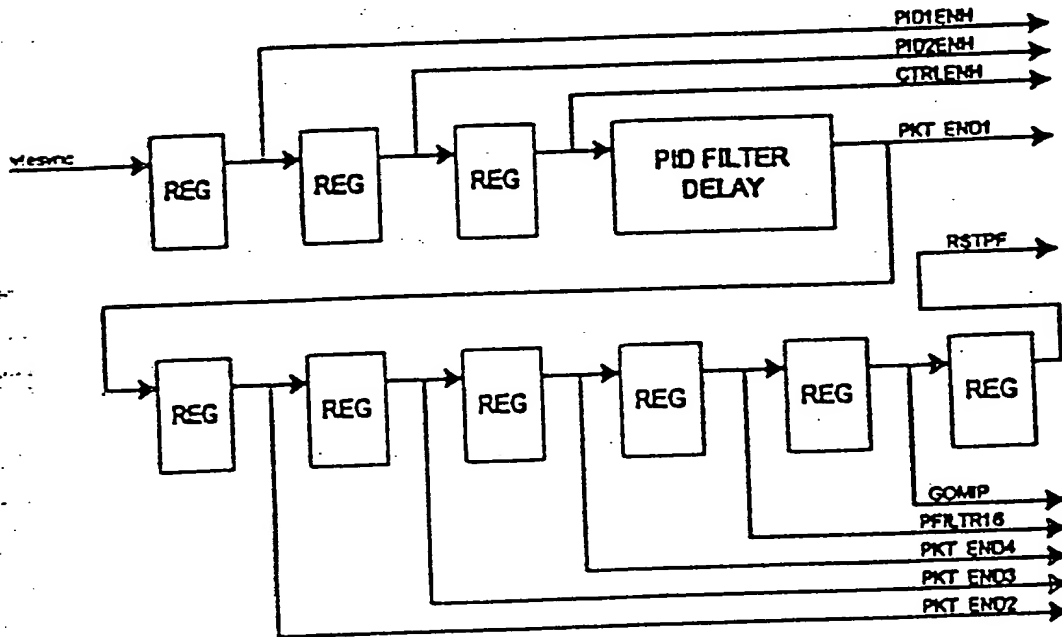
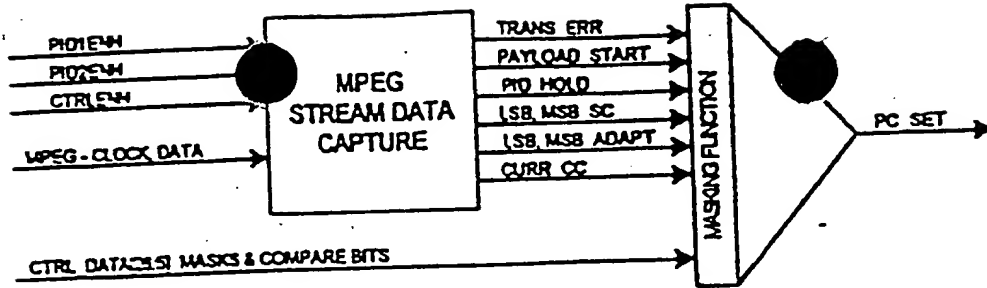


Figure 4b

00604287-100600

00900T 28 24 29 60

MPEG HEADER

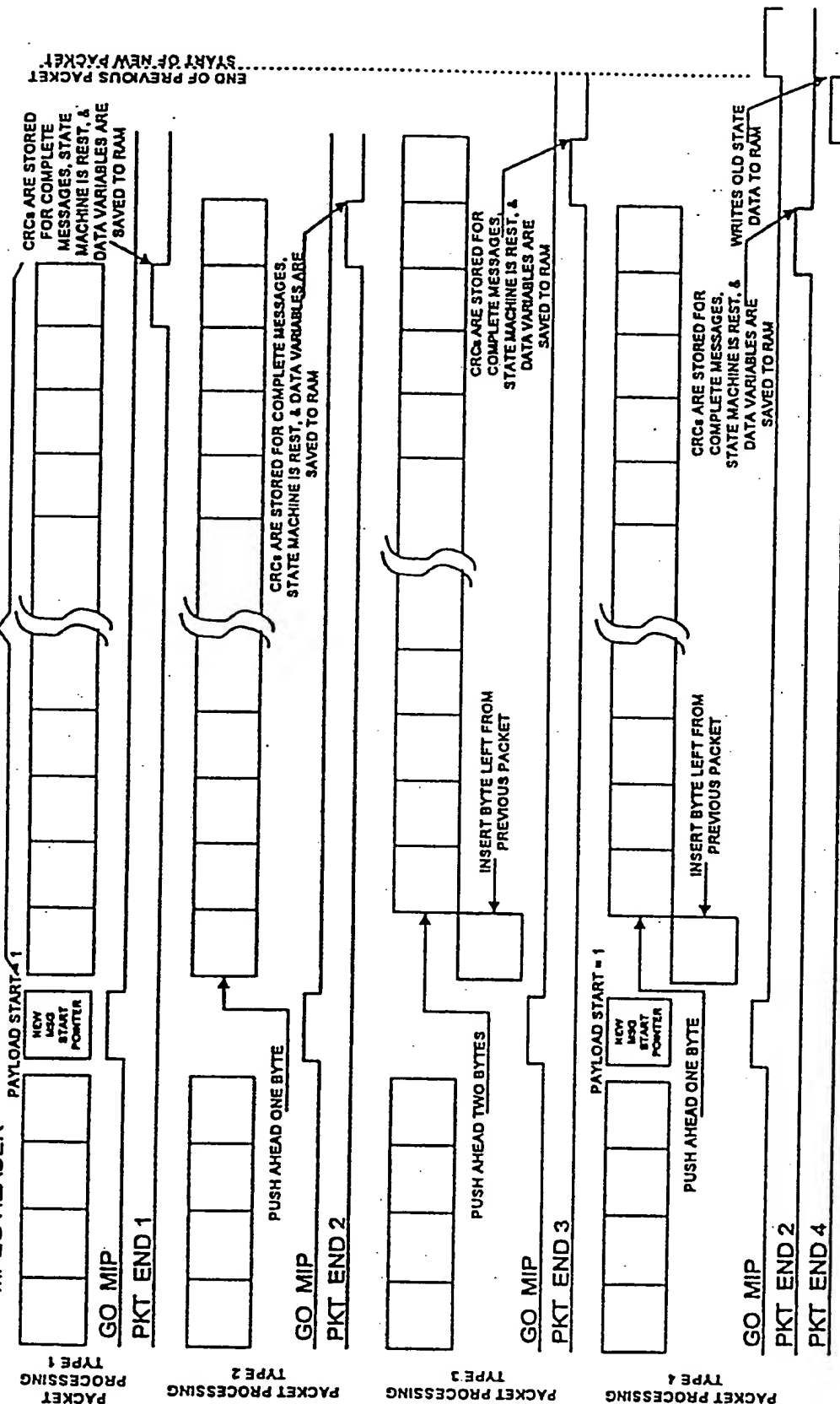


Figure 5

096467 10600

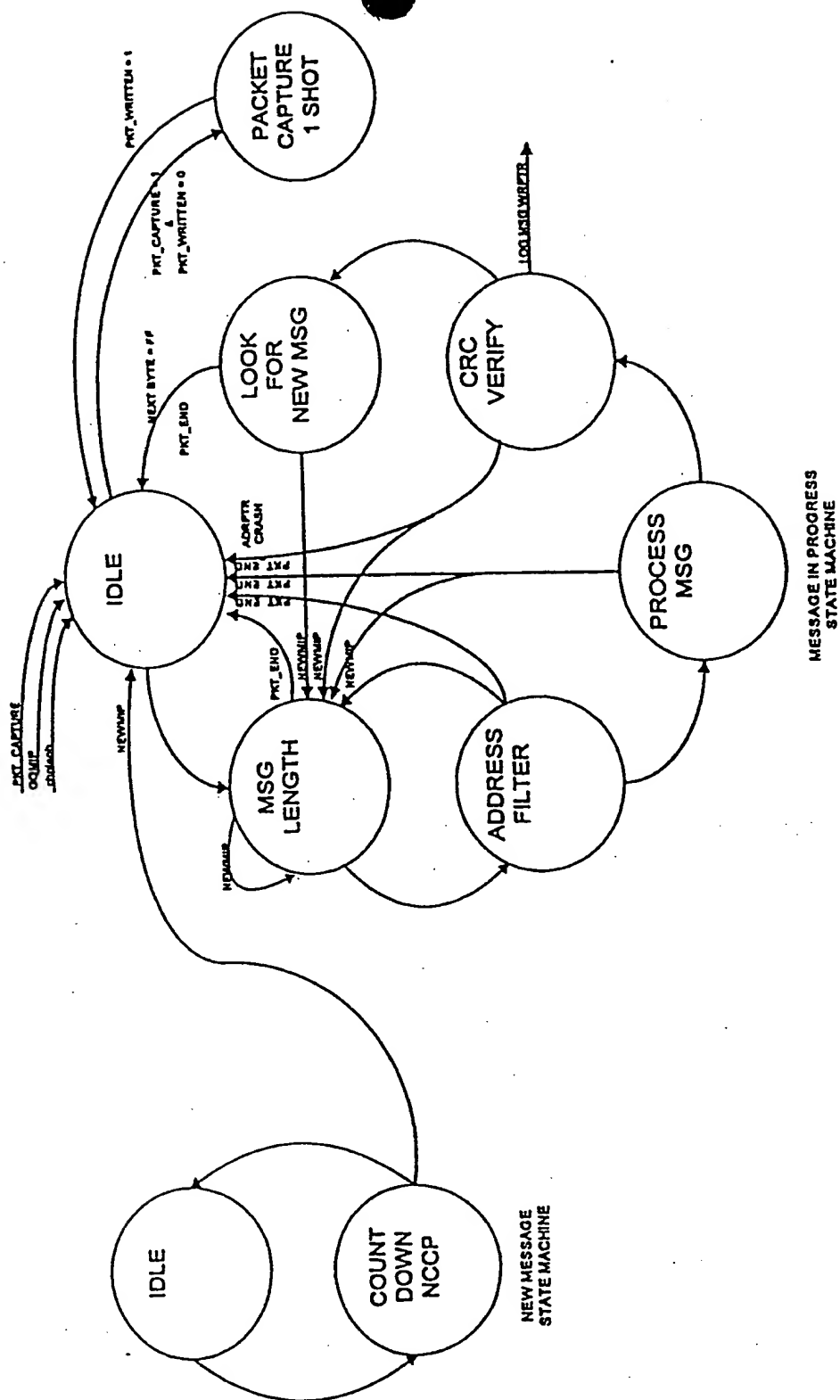
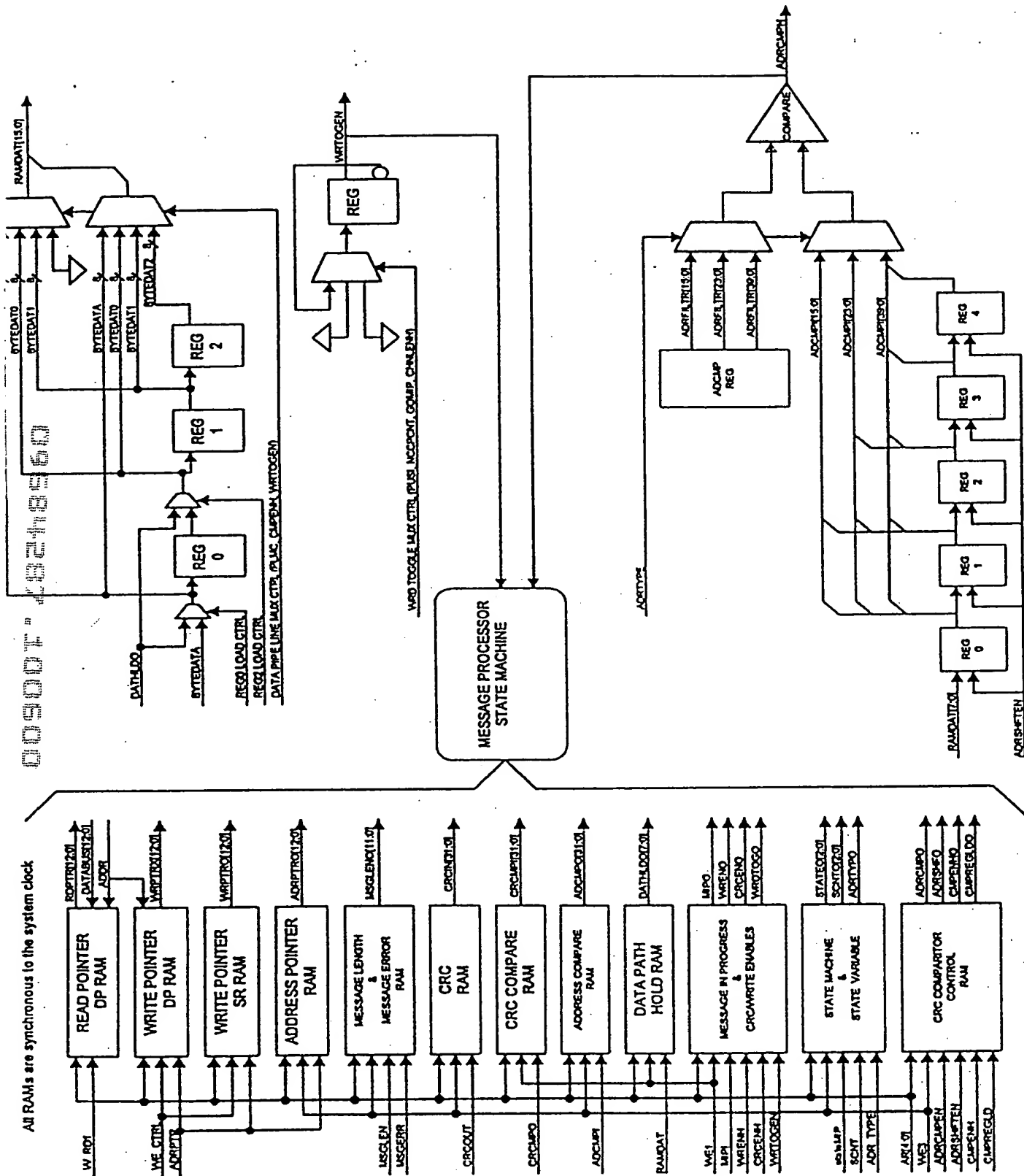


Figure 6

All RAMs are synchronous to the system clock



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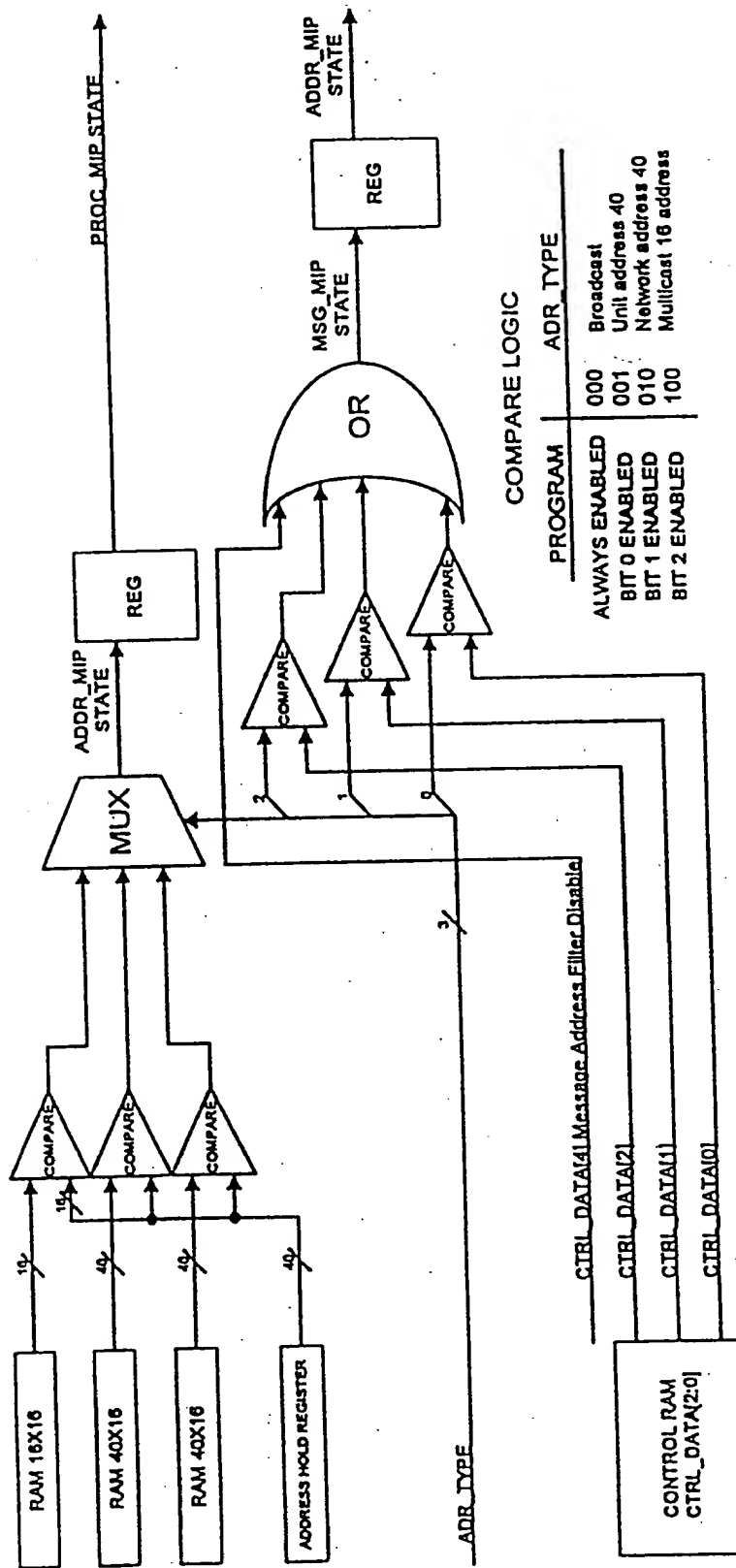
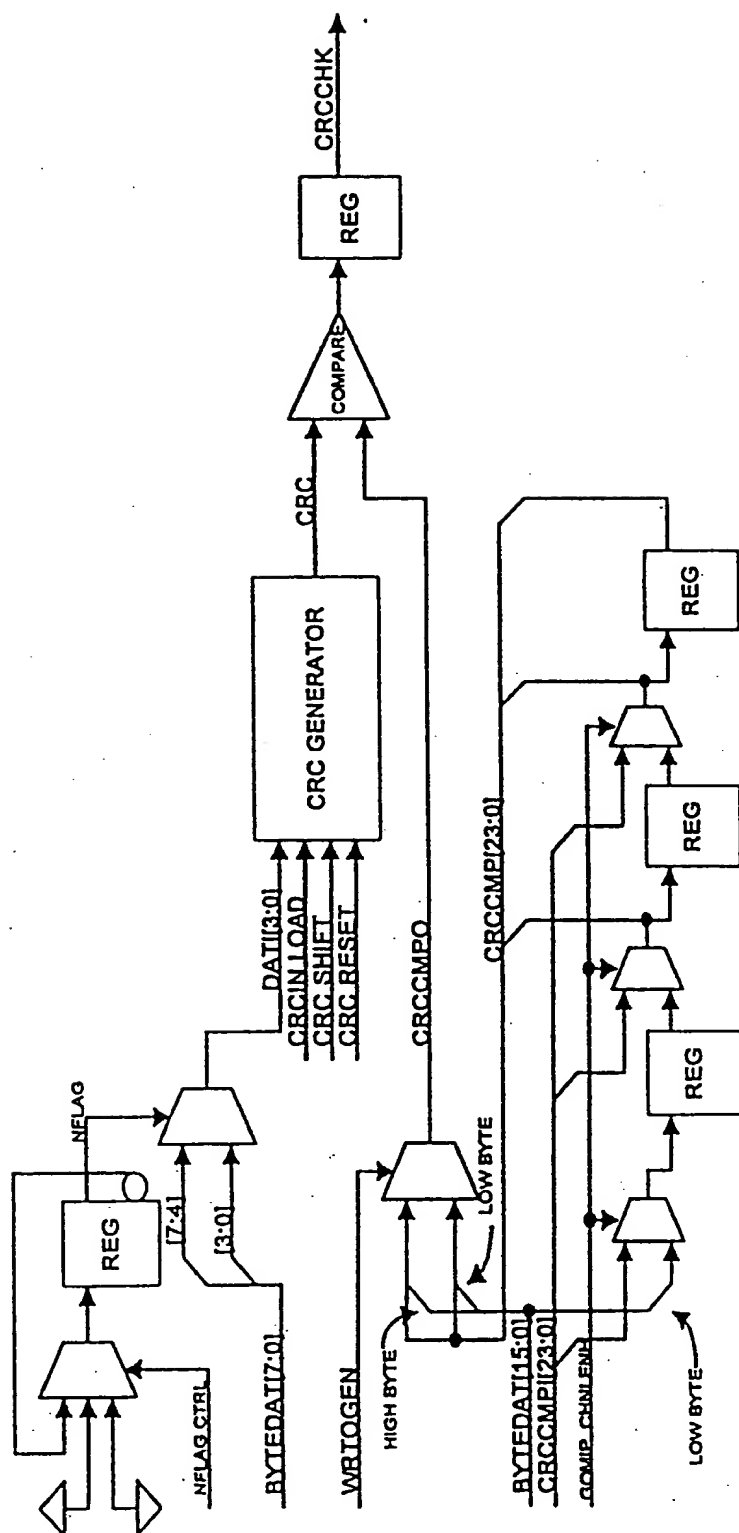


Figure 8

Figure 9



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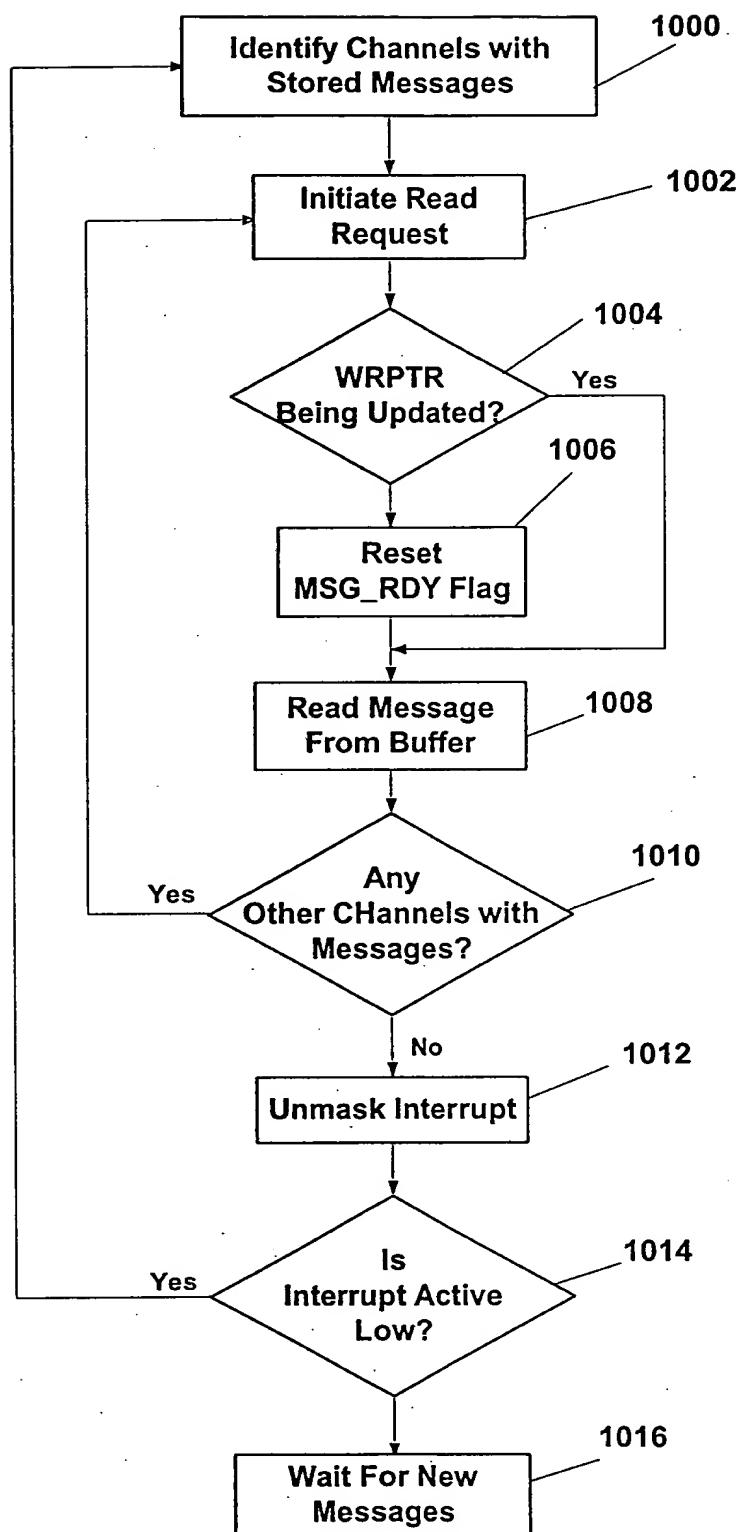


Fig. 10

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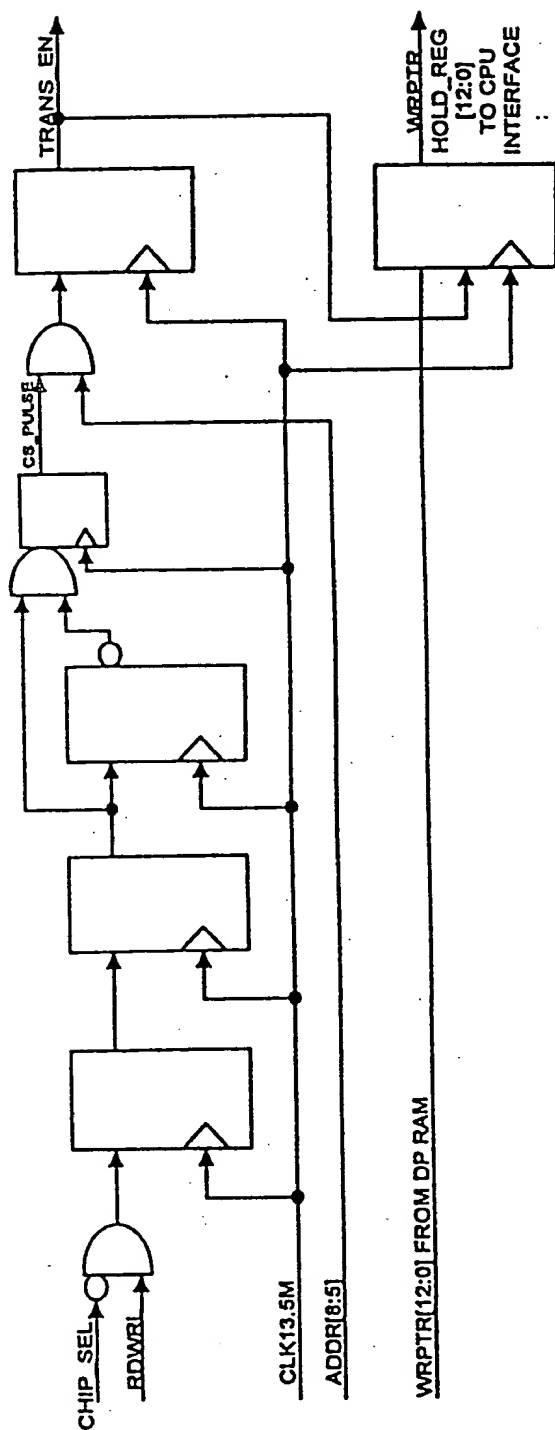


Figure 11

Figure 12